

processing unit being constructed at least mainly of at least substantially asynchronously operating logic components (asynchronous logic).

2. A data carrier as claimed in Claim 1, characterized in that the contactless interface and the data processing unit are coupled to one another via an asynchronous transmission/receiving circuit which is included in the data processing unit.

3. A data carrier as claimed in Claim 1, characterized in that individual stages within at least the data processing unit operate in a time interleaved manner.

4. A data carrier as claimed in Claim 1, characterized in that the contactless interface for the electrical energy for the operation of the data processing unit has the function of an at least substantially ideal current source.

IN THE ABSTRACT

Please delete in its entirety and replace with the following:

--A data carrier is disclosed. The data carrier includes a data processing unit and at least one contactless interface via which the data processing unit can be coupled to a read/write apparatus in order to exchange data signals and to take up electrical energy for the operation of the data processing unit; the data processing unit is constructed at least mainly while using at least substantially asynchronously operating logic components (asynchronous logic). The data carrier according to the invention, such as a chip card, makes optimum use of